

### Abstract

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A substrate layer is provided at the rear side of a single semiconductor element. An active layer is arranged between the substrate layer and a contact side of the single semiconductor element. At least two solder contacts are electrically connected to the active layer and project beyond the contact side in order to make possible a direct soldering of the single semiconductor element to a carrier board. The contact side is provided with a glass passivation layer. Alternatively or additionally to this, at least one side face of the single semiconductor element is provided with an insulator layer in order to avoid short circuits on the soldering of the single semiconductor element to the carrier board. It is furthermore possible for the solder contacts to have different outlines at the contact side.

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